

FIG. 1

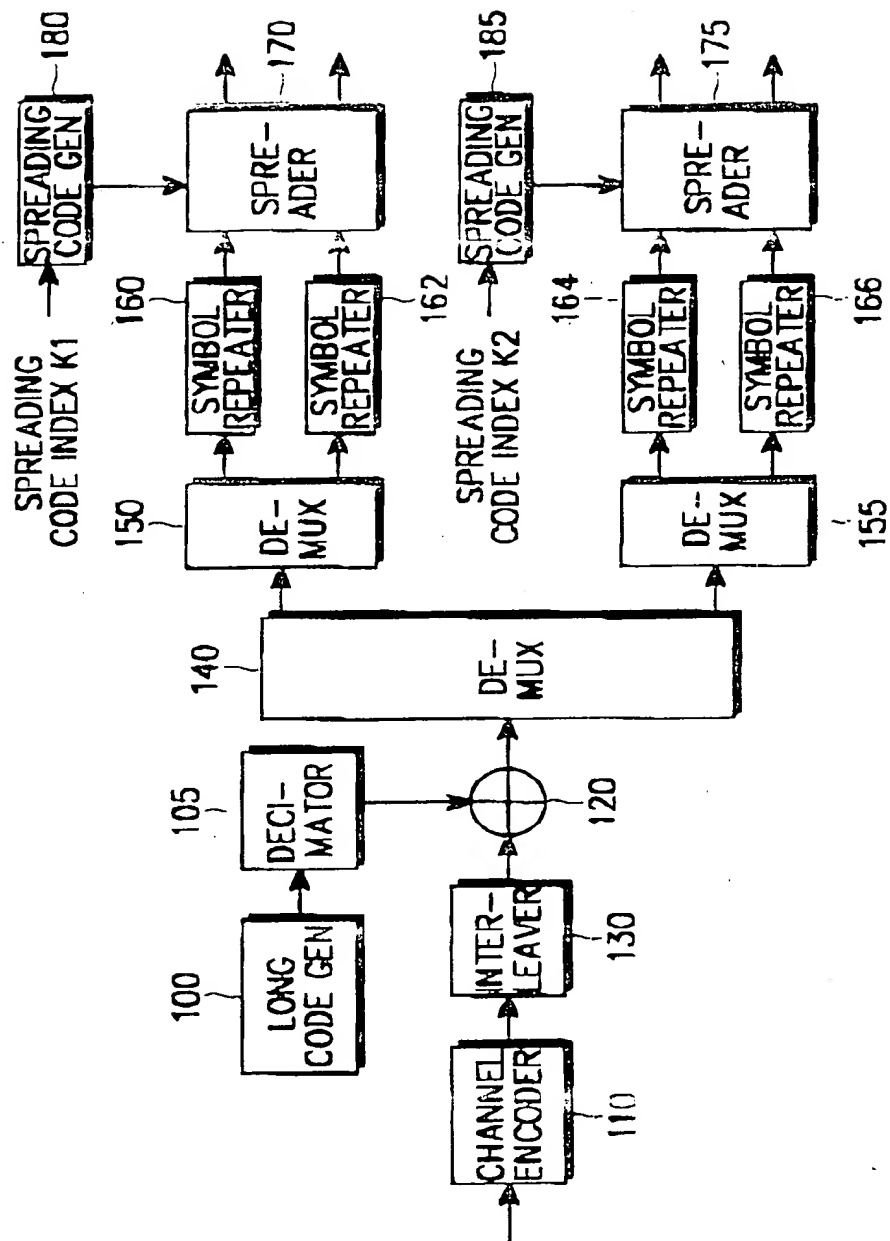


FIG. 2

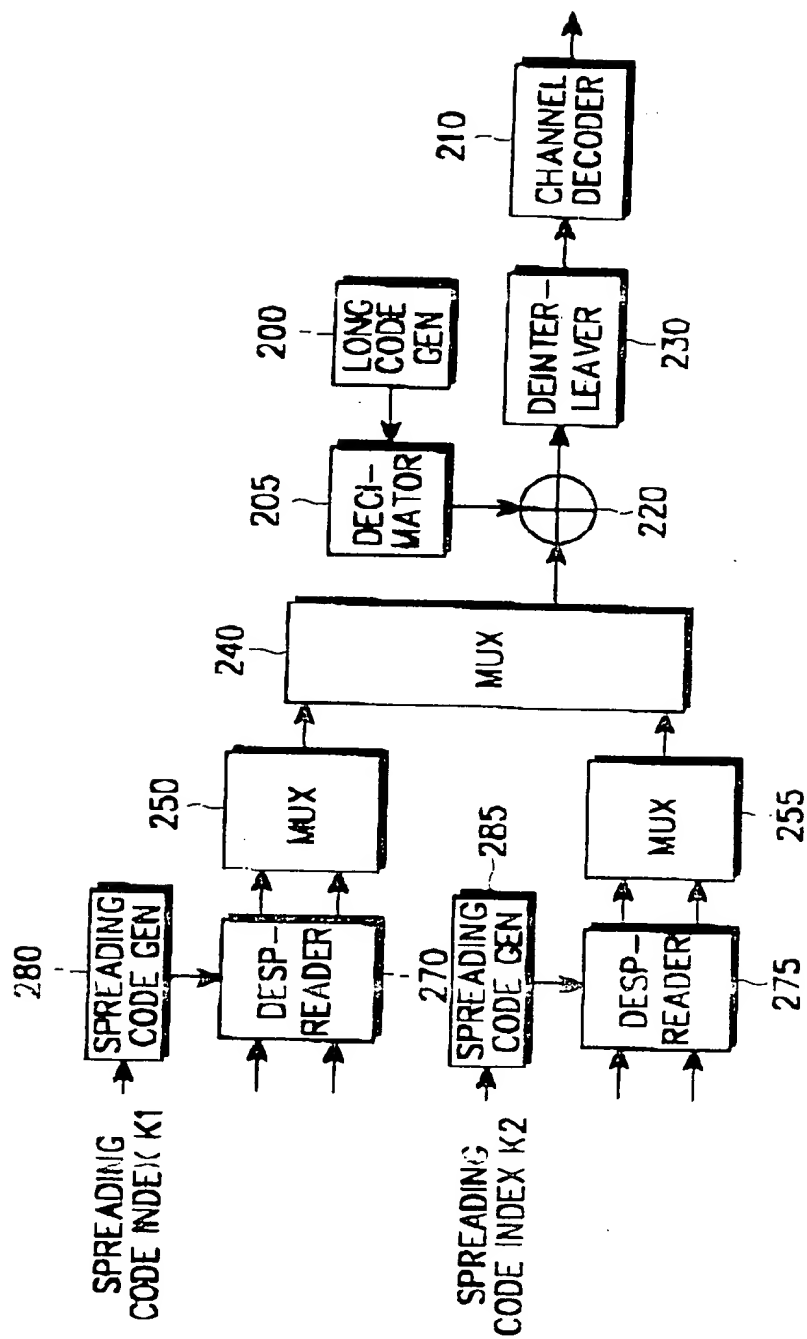


FIG. 3

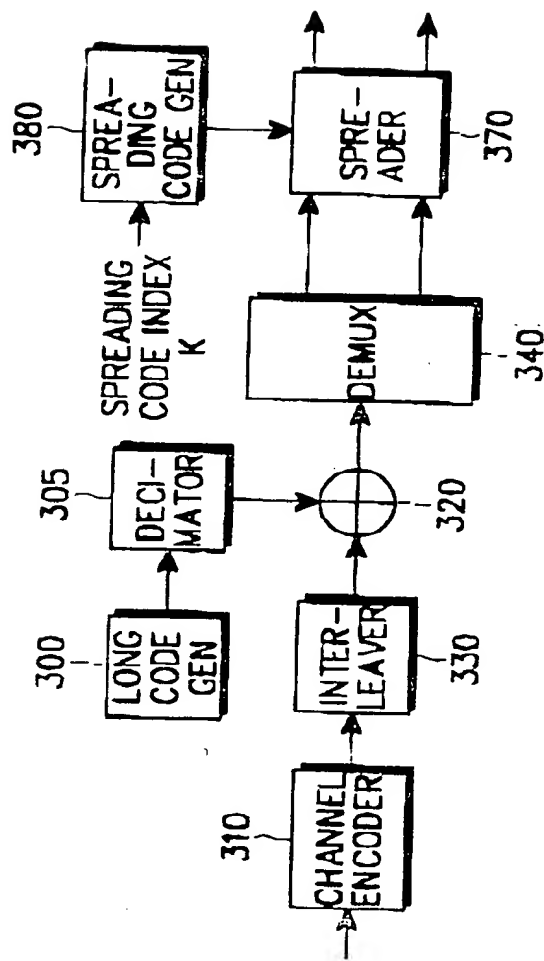


FIG. 4

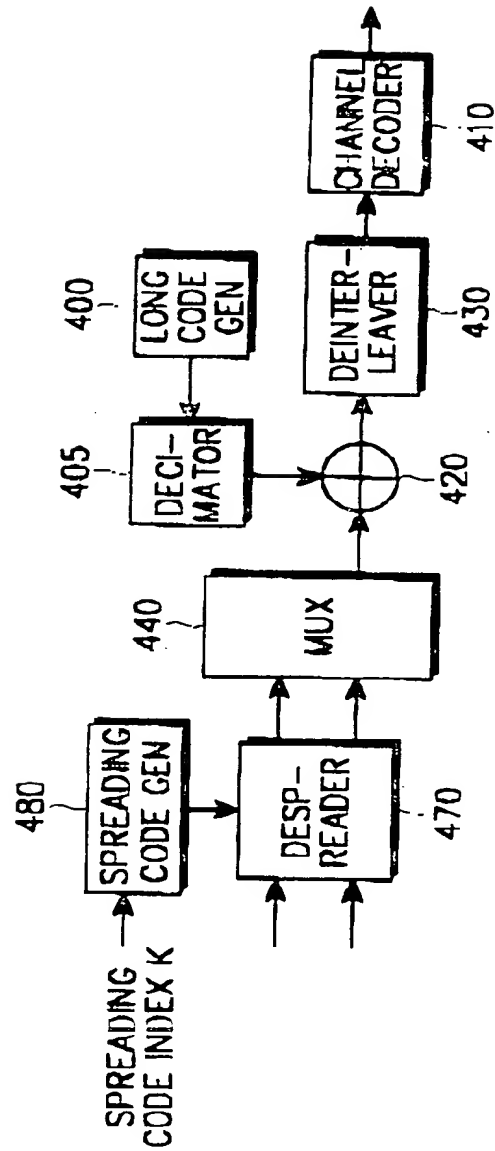


FIG. 5

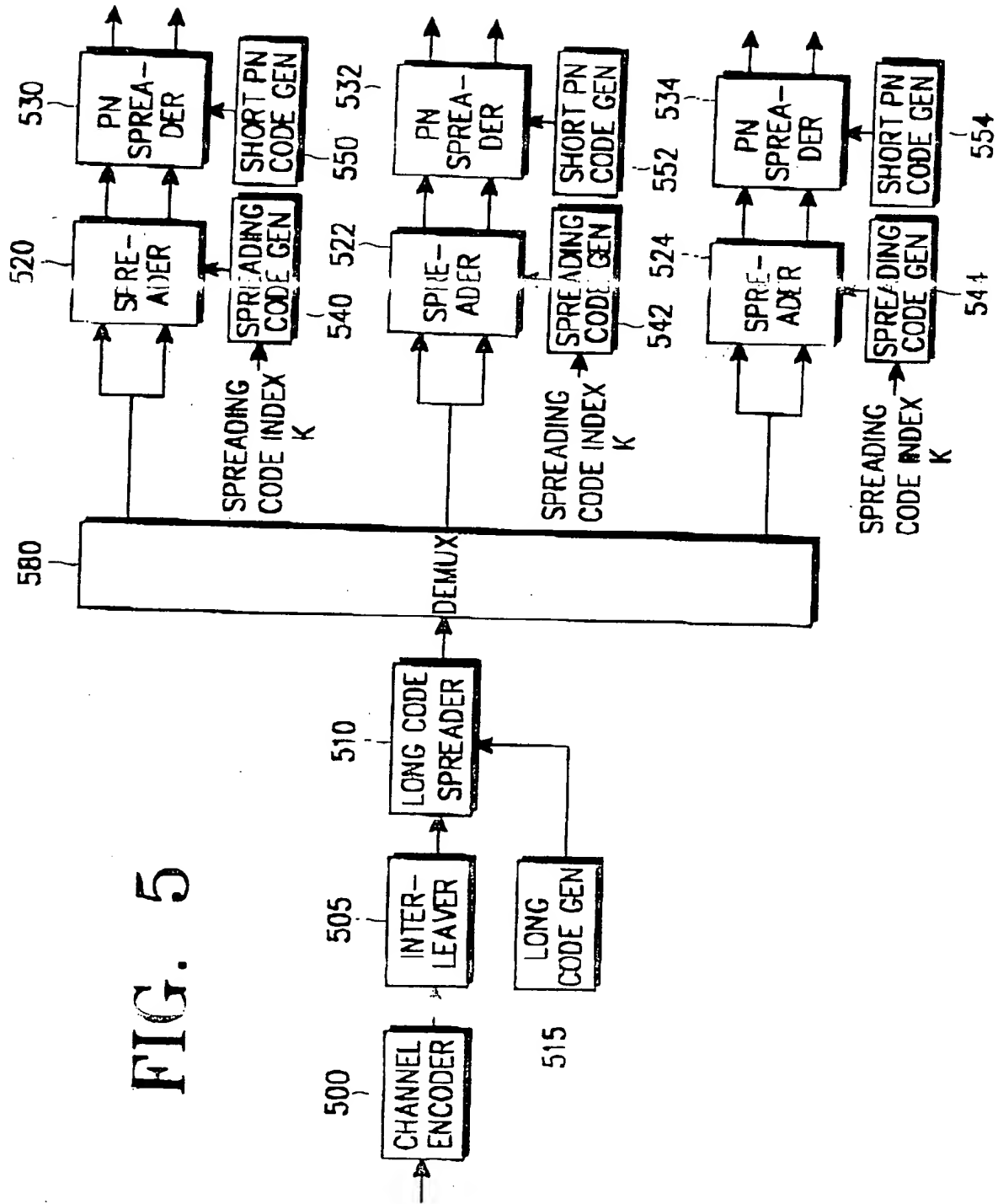
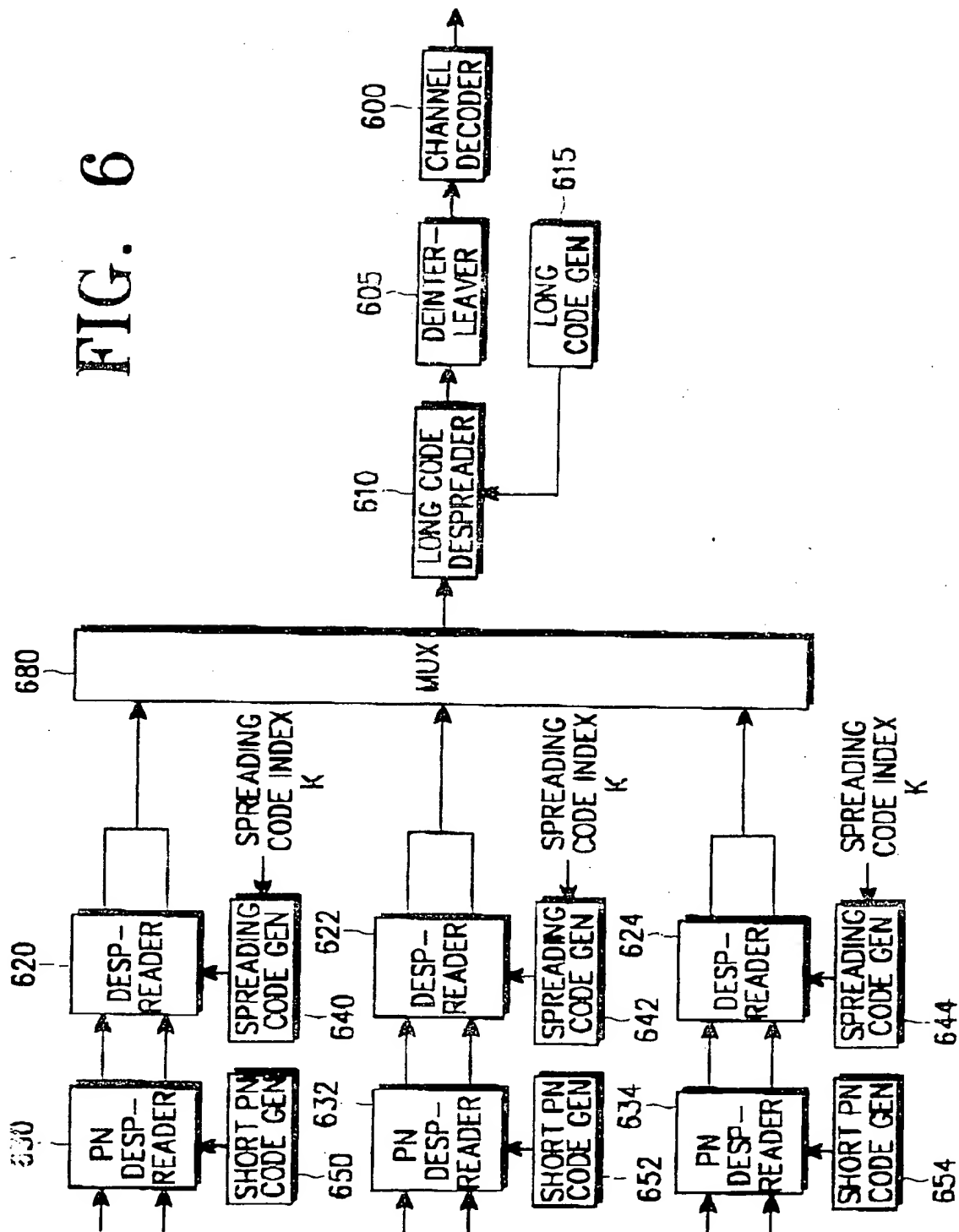


FIG. 6



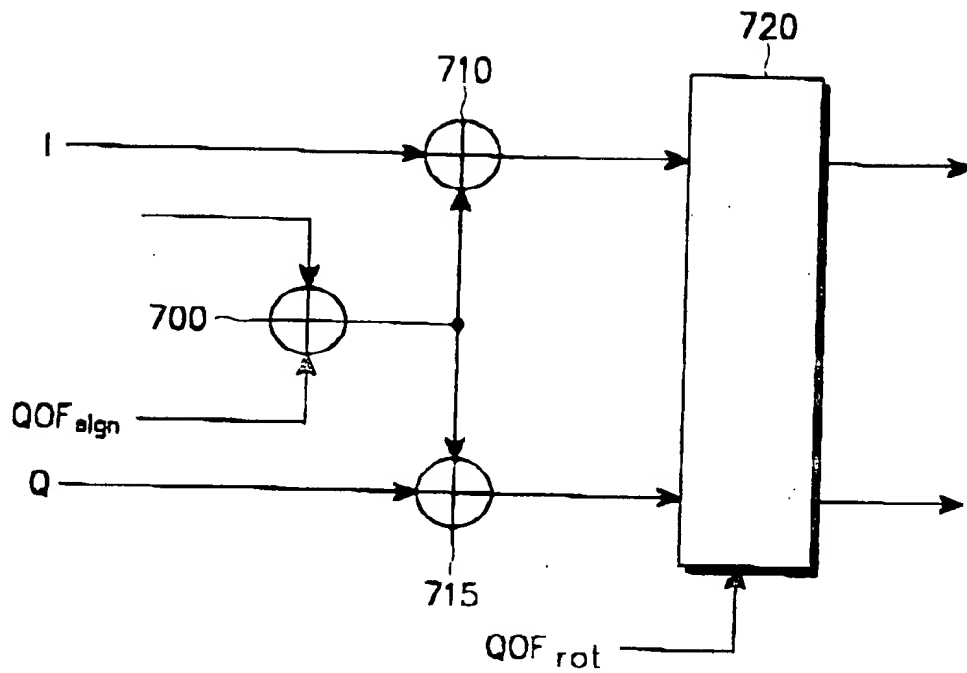


FIG. 7

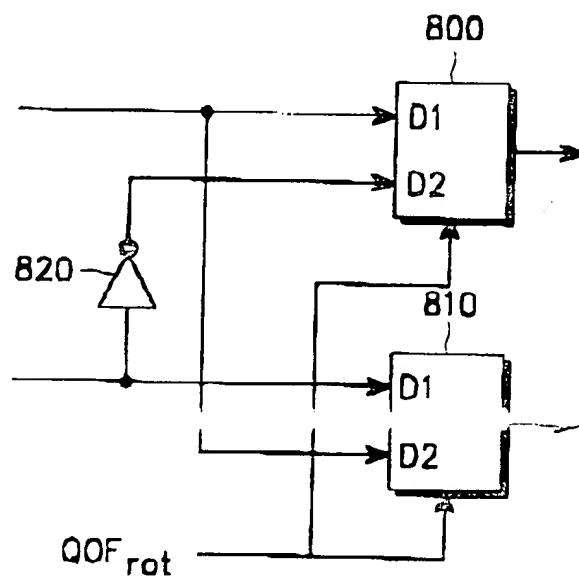


FIG. 8

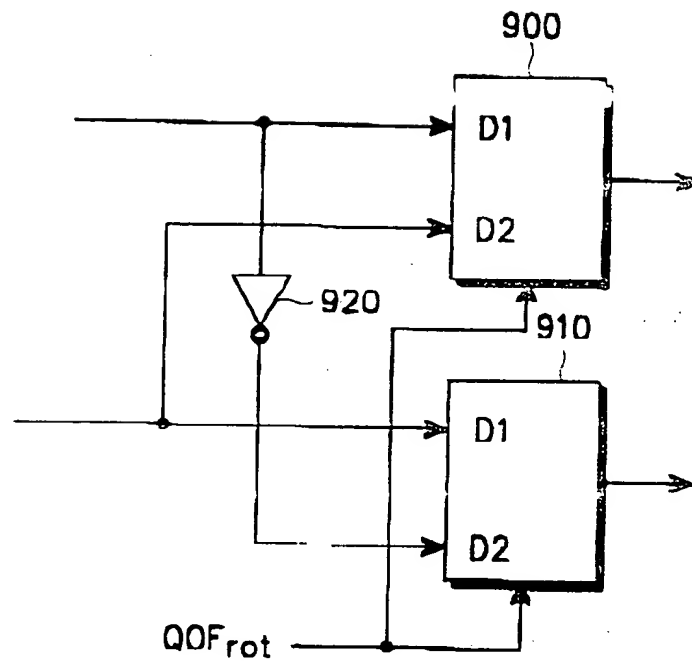


FIG. 9

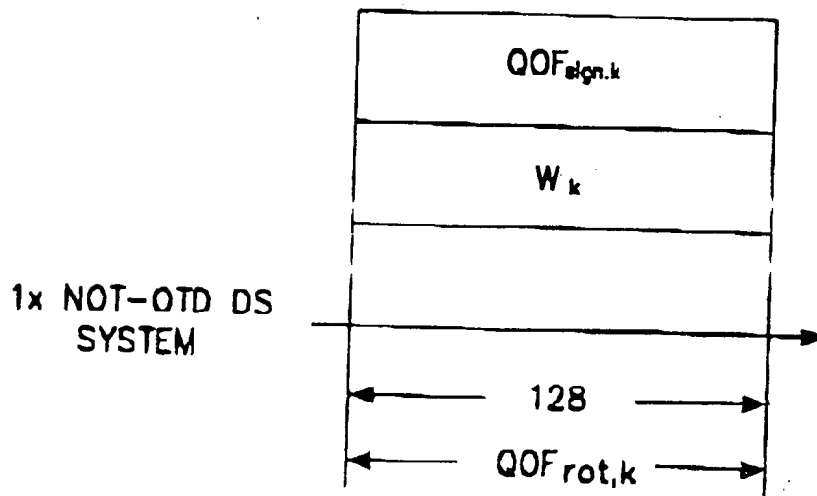


FIG. 10A

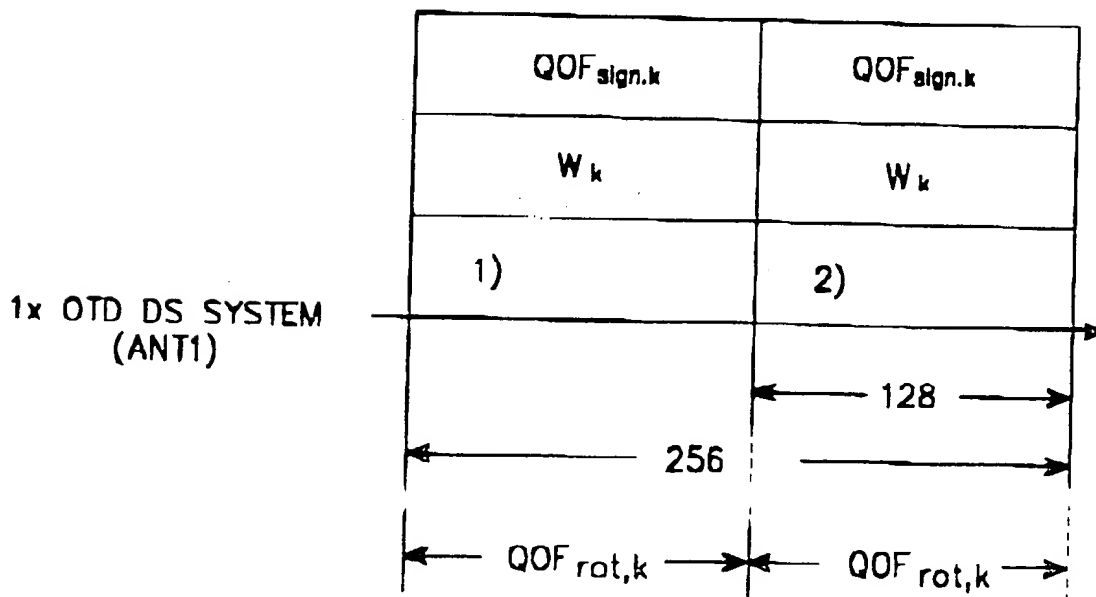


FIG. 10B

1x OTD DS SYSTEM
(ANT2)

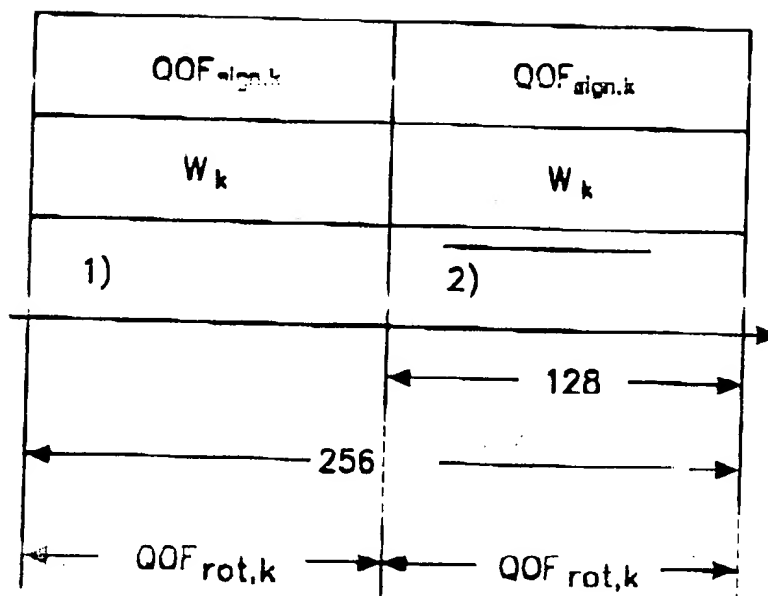


FIG. 10C

3x NON-OTD DS
SYSTEM

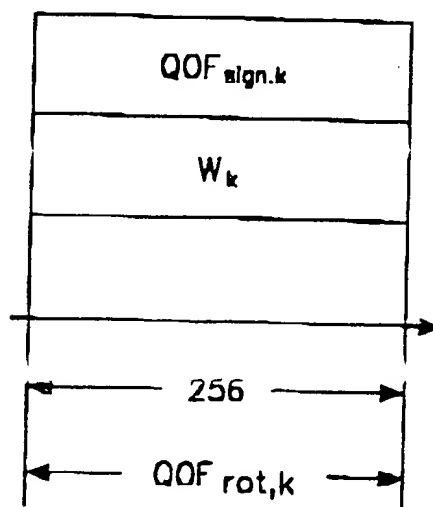


FIG. 10D

3x OTD DS
SYSTEM (ANT1)

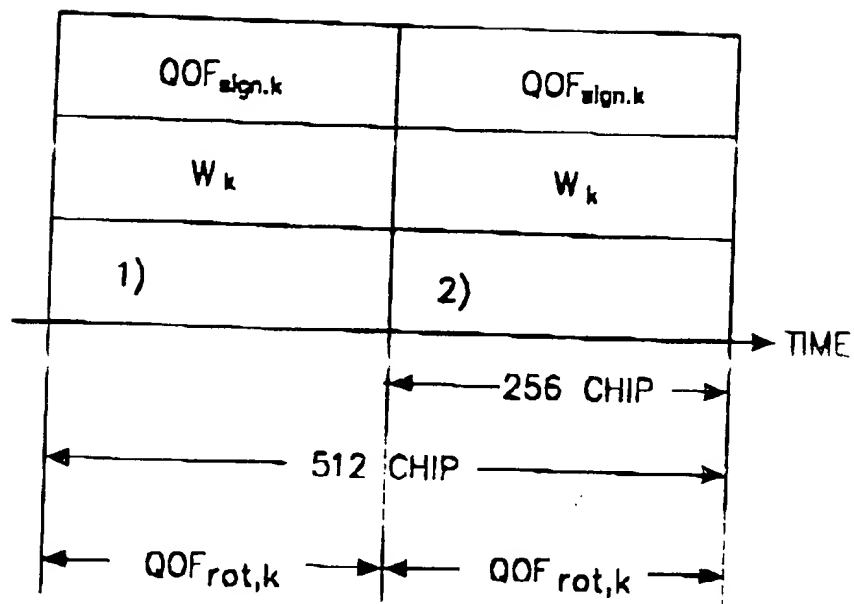


FIG. 10E

3x OTD DS
SYSTEM (ANT2)

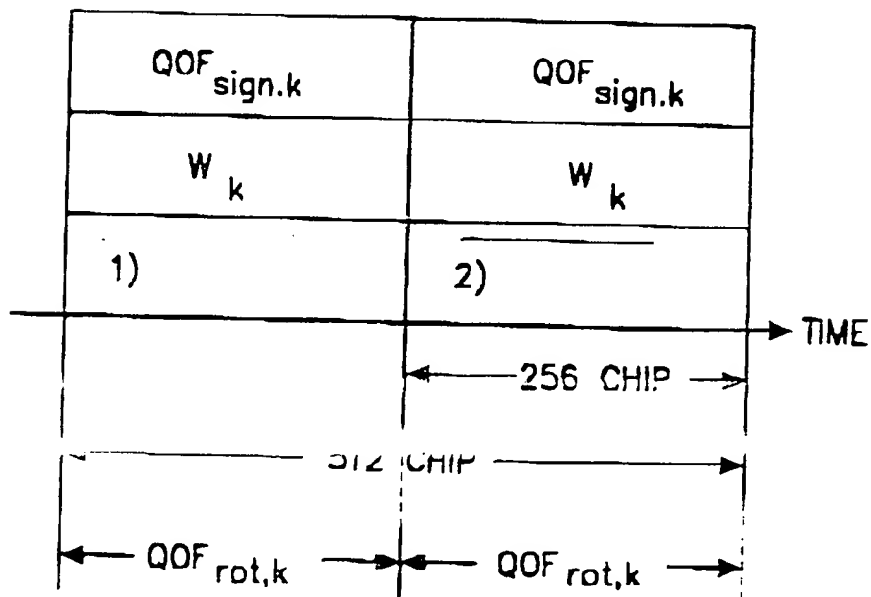


FIG. 10F

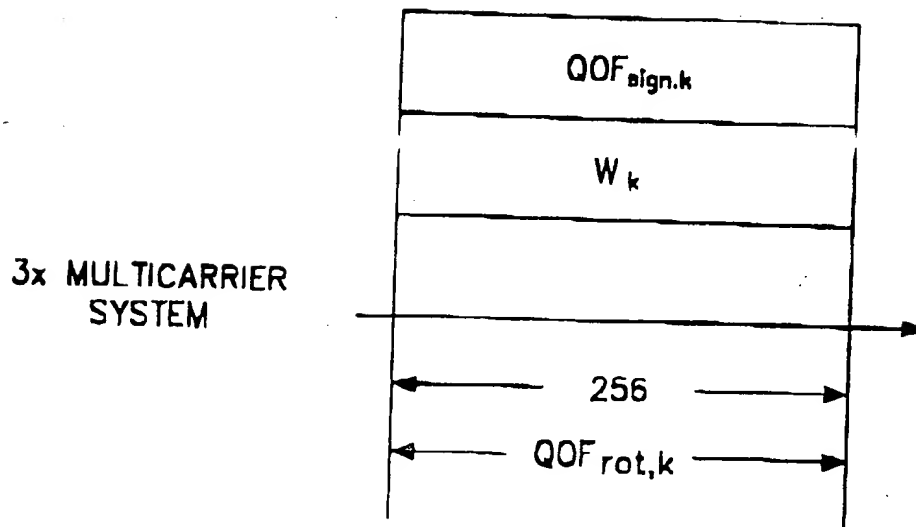


FIG. 10G

1x NON-OTD DS
SYSTEM

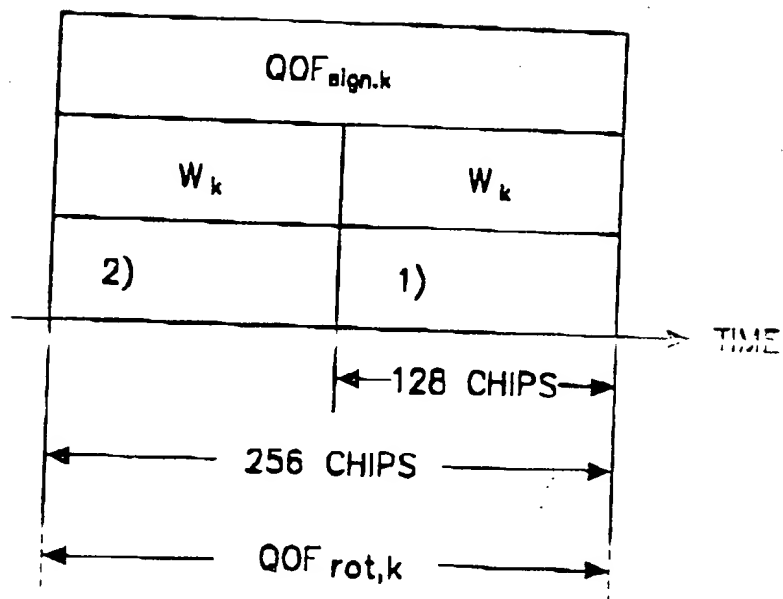


FIG. 11A

1x OTD DS
SYSTEM (ANT1)

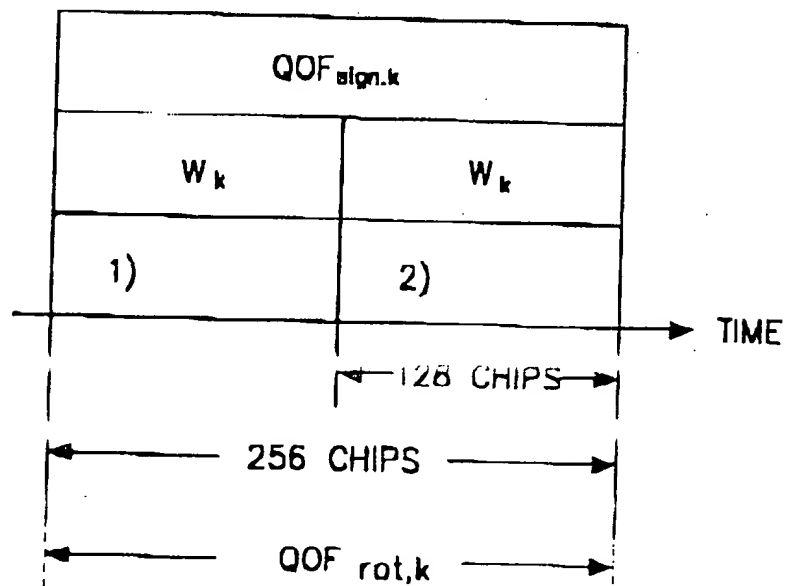


FIG. 11B

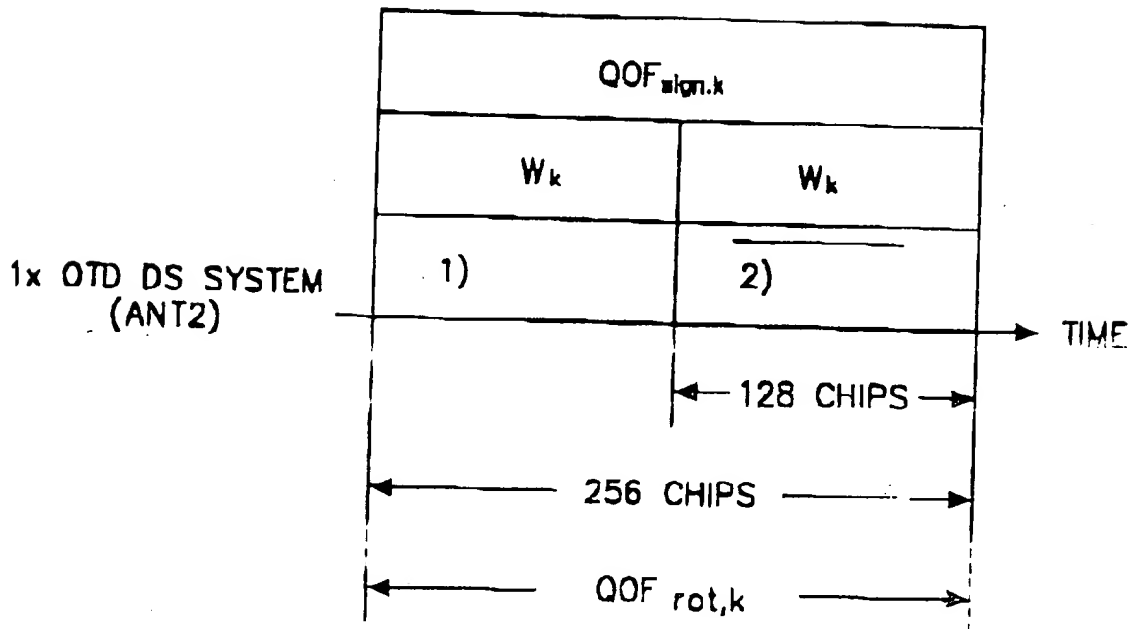


FIG. 11C

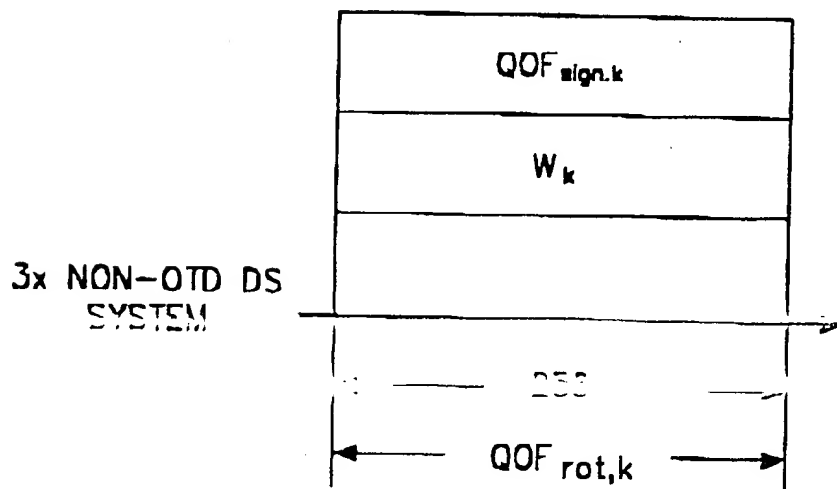


FIG. 11D

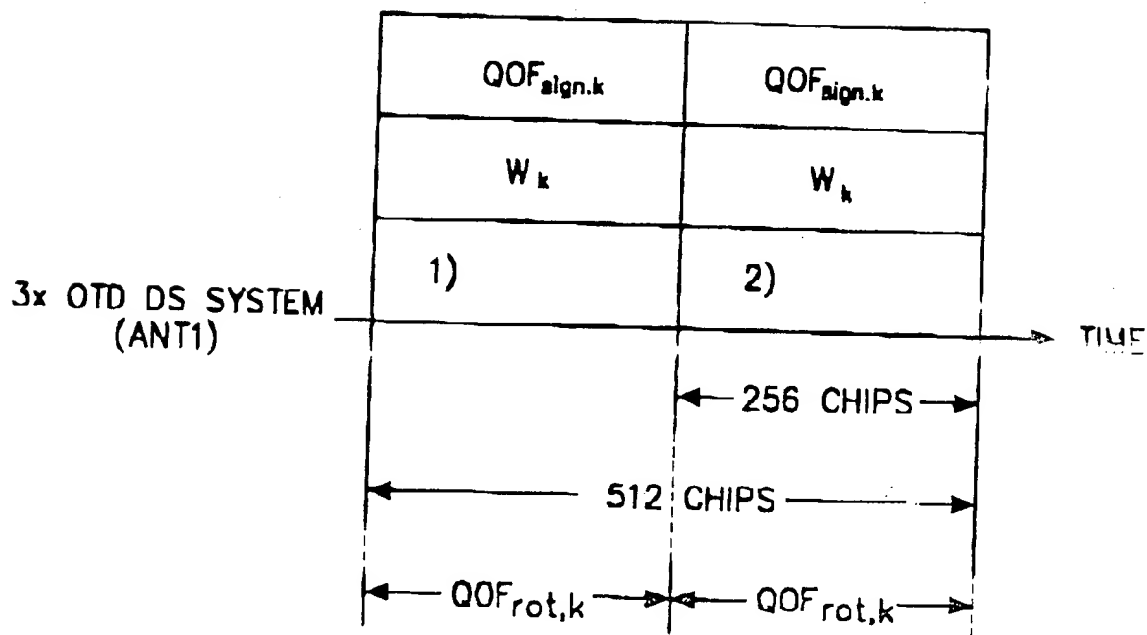


FIG. 11E

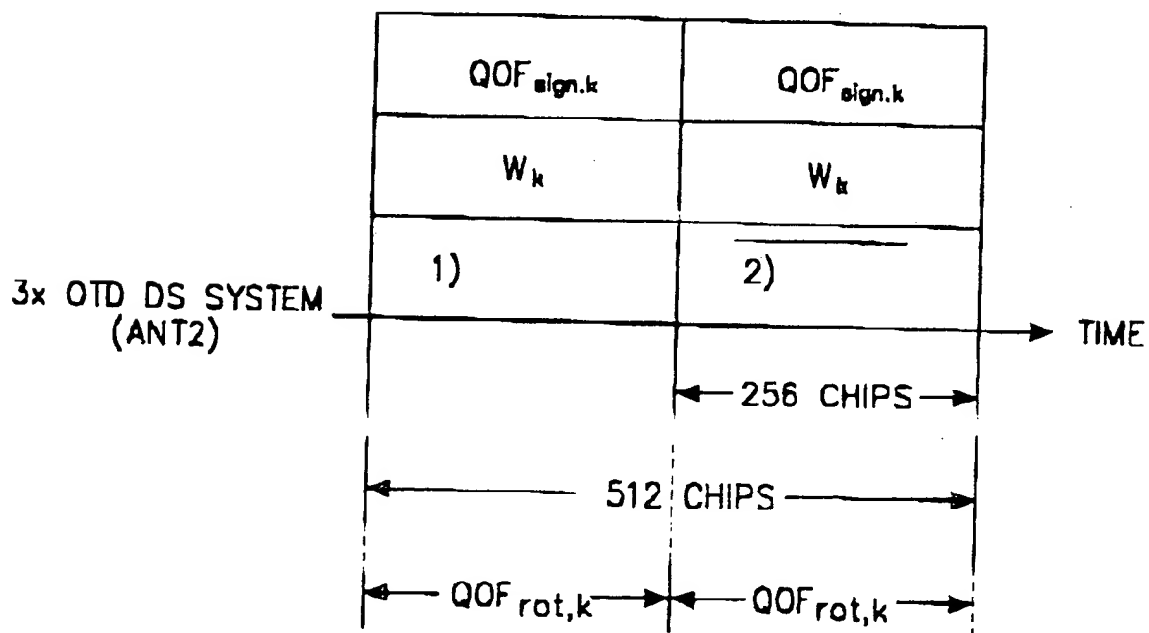


FIG. 11F

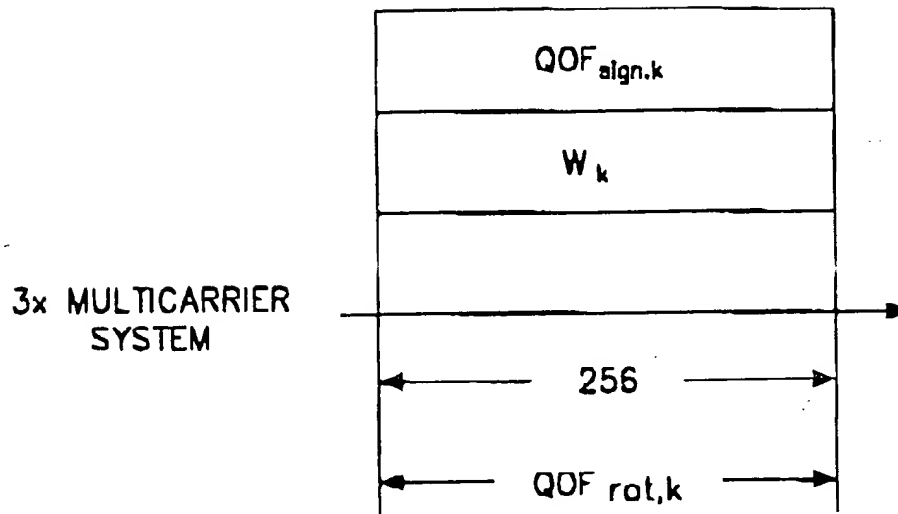


FIG. 11G

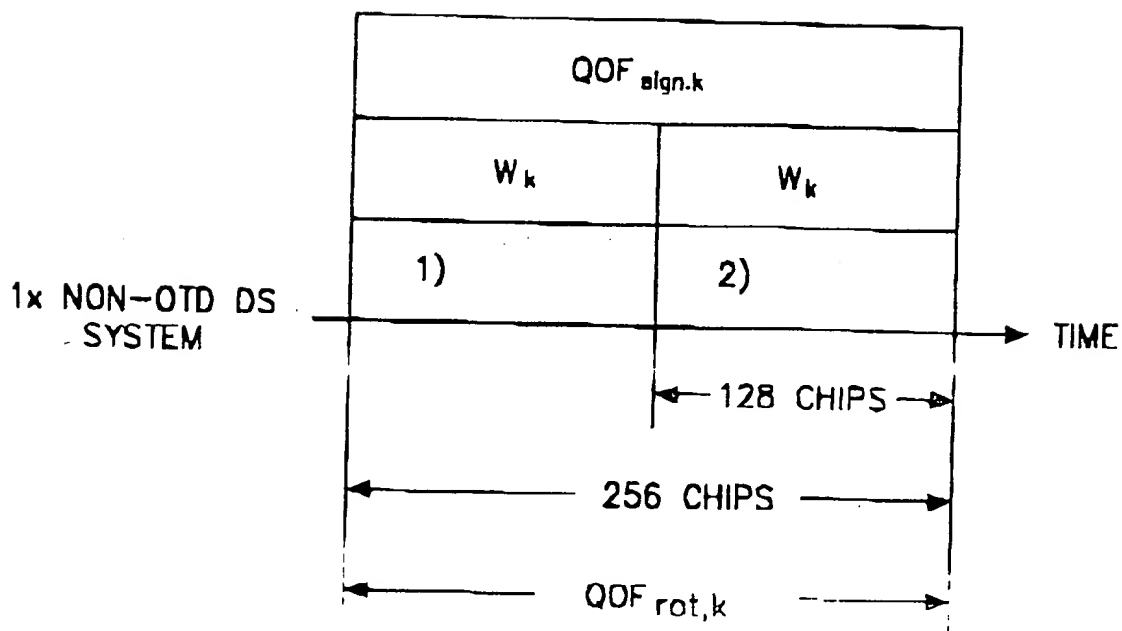


FIG. 12A

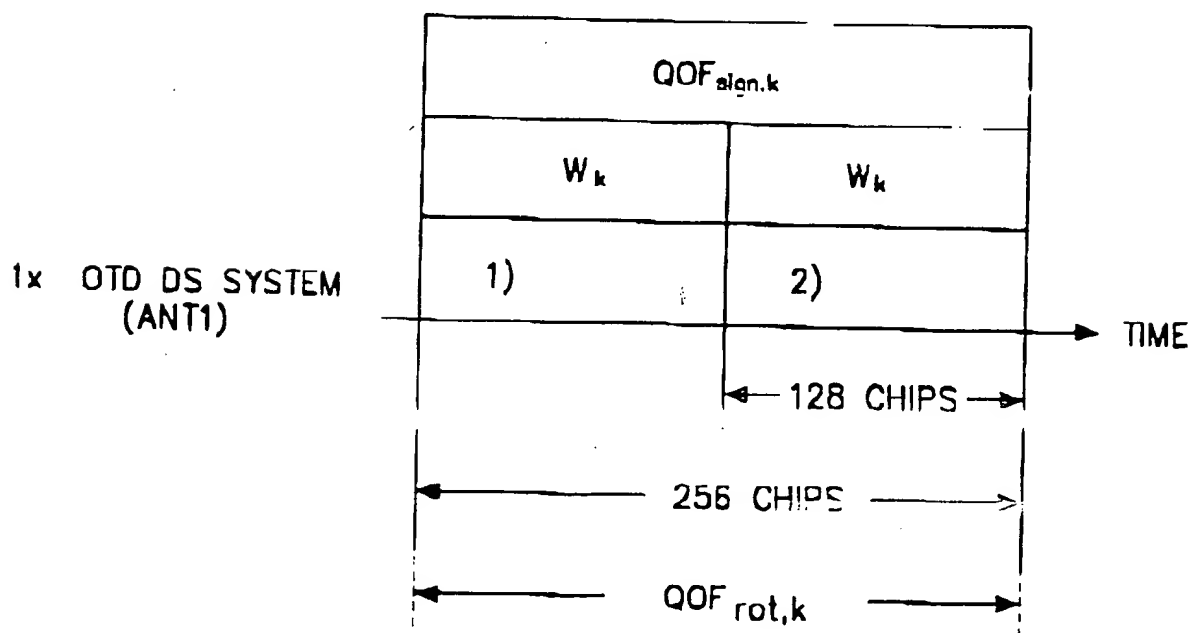


FIG. 12B

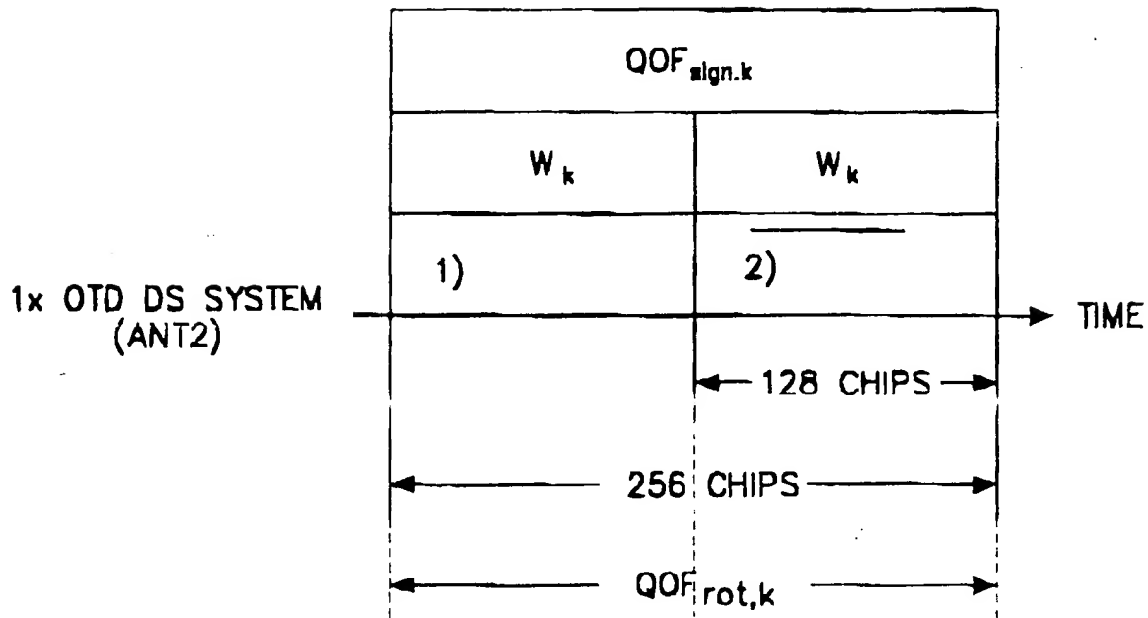


FIG. 12C

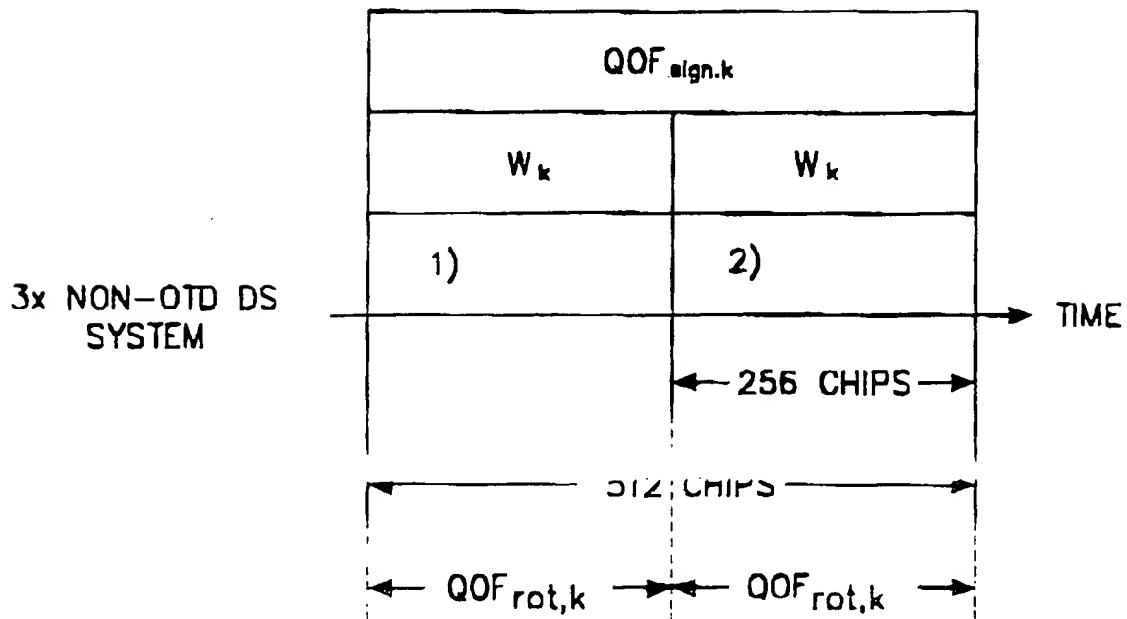


FIG. 12D

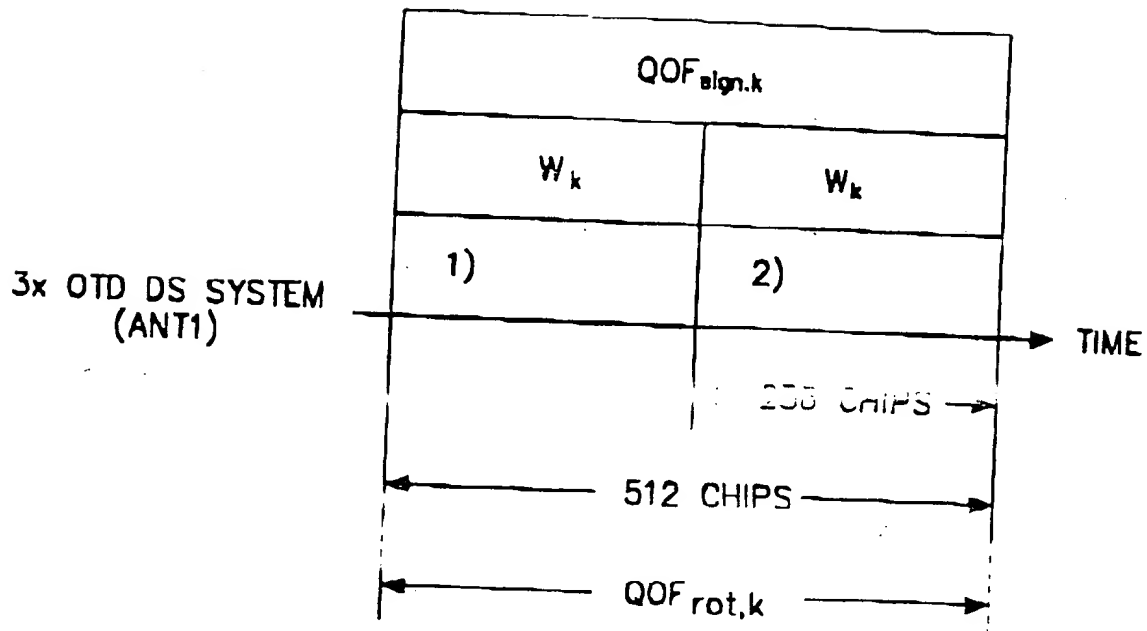


FIG. 12E

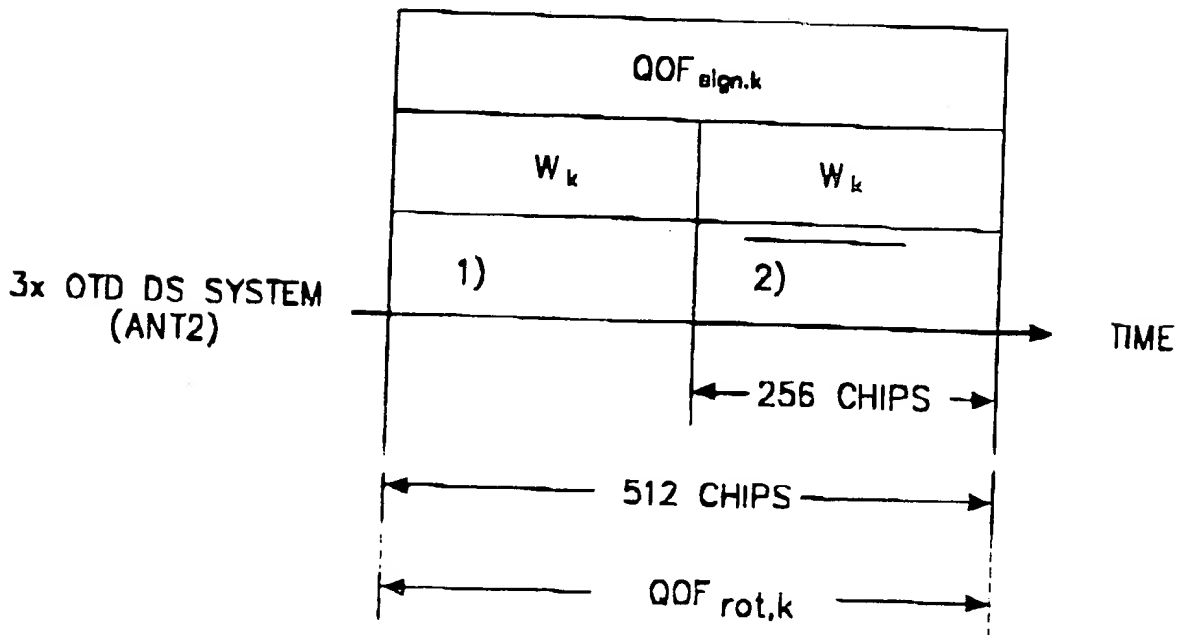


FIG. 12F

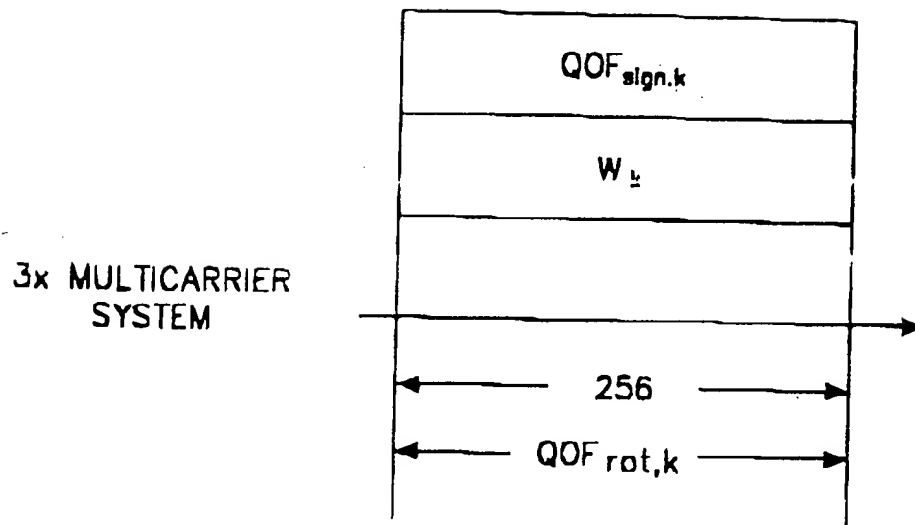


FIG. 12G